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20350 7590 04/06/2005

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Gloria Sikora	(Depositor's name)
<i>Gloria Sikora</i>	(Signature)
June 29, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/077,258	02/14/2002	Brian Sze-Ki Mo	018865-001010US	5261

TITLE OF INVENTION: METHOD OF FORMING A TRENCH TRANSISTOR HAVING A SUPERIOR GATE DIELECTRIC

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/06/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
HUYNH, YENNHU B	2813	438-270000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Fairchild Semiconductor Corporation

South Portland, Maine

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

- ☒ Issue Fee
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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Date July 29, 2005

Typed or printed name

Barmak Sani

Registration No. 45,068

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